

Electronic Acknowledgement Receipt

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Application Number:	10807648
International Application Number:	
Confirmation Number:	3901
Title of Invention:	Cached memory system and cache controller for embedded digital signal processor
First Named Inventor/Applicant Name:	Gilbert Christopher Sih
Customer Number:	23696
Filer:	Joseph B. Agusta/Theresa Boyce
Filer Authorized By:	Joseph B. Agusta
Attorney Docket Number:	030459
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Amendment After Final	030459_RFOA_07-07-08.pdf	159041 15e06bc120802f61107f9a7b4cead3aa31d75eod	no	16

Warnings:

Information:

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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